WEST Search History

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DATE: Friday, June 09, 2006

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		DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR	
☐	L15	L12 and (clock same cycle)	48
☐	L14	L12 and (clock same cycle) and fpga	2
☐	L13	emulator same clock same share same (dut or test$ or device)	43
☐	L12	emulator same clock same share	51
☐	L11	L1 and (shar$4 adj3 clock)	7
☐	L10	L1 and (clock adj4 shar$4)	6
☐	L9	L4 and (clock adj4 shar$4)	0
☐	L8	L4 and (clock adj shar$4)	0
☐	L7	L6 and (interrupt or exception)	8
☐	L6	L4 and (clock same shar$4)	22
☐	L5	L4 and share	31
☐	L4	L3 and (clock adj cycle)	45
☐	L3	L2 and fpga	71
☐	L2	L1 and (clock same signal)	214
☐	L1	clock$ same emulat$3 same (share or sharing or shared or distribut$)	235

END OF SEARCH HISTORY